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01_Revision_History.SchDoc

Revision History

Rev.	Rel.	Date	Author	Description	Page(s)
1.0.0	A	2021-05-15	EF	- Initial design	4
2.0.0	B	2023-06-13	EF	- Replaced obsolete components - Changed G1, added test point for 5V	4

RIC7S113EVAL1

Default_Assembly

B

IR HiRel Products

IRHIR PSS HiRel S&M M TM

101 N Pacific Coast Highway El Segundo, CA 90245

06/2023

Link to Top Level Document

02 Top Level
02_Top_Level.SchDoc

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Title	RIC7S113EVAL1		
Variant	Default_Assembly	Approved	<Appr.>
Size: A3	Document Name: 01_Revision_History.SchDoc	Rel. EF	Rev. B
Author: Eric Faraci			
Date: 6/13/2023 Time: 7:23:06 PM	SVN Revision: Not in version control		

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02_Top_Level.SchDoc

Miscellaneous
Miscellaneous.SchDoc

Schematic
03_Schematic.SchDoc

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Title	RIC7S113EVAL1		
Variant	Default_Assembly	Approved	<Appr.>
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03_Schematic.SchDoc

HIN
PWM Input
0V to VDD

IN/LIN
PWM Input
0V to VDD

SD
Input
0V to VDD

VCC
15V Nominal
10V - 20V
-

VDD
5V Nominal
5V - 20V
-

HV
400V MAX
+ -

SW
400V MAX

RIC7S113A4

SN74LVC2G14DCKR

TLE7274-2D

BAS3010B-03W

BUY65CS08J-01(ES)

US1J-E3/61T

IFX-TestPad

NC_EXCEPTION

22-28-4023

282836-2

CONBNC001

575-6

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Miscellaneous.SchDoc

MP1 MP2 MP3 MP4

9434 9434 9434 9434

MP5 MP6 MP7 MP8

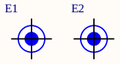
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X33 X34

X35 X36

Fiducial Markers

E1 E2

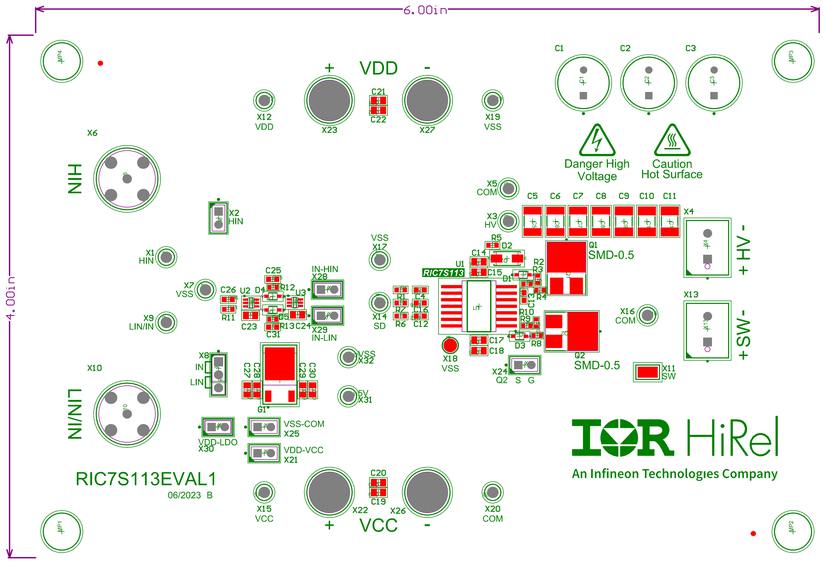


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IRHIR PSS HiRel S&M M TM
RIC7S113EVAL1
Application Board
EASY 12345 B 06/2023

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IR HiRel Products IRHIR PSS HiRel S&M M TM 101 N Pacific Coast Highway El Segundo, CA 90245	ENGINEER:	TITLE: RIC7S113EVAL1		
	PCB DESIGNER: Eric Faraci			
	DATE: 6/13/2023	PART NO.:	REL: B	REV: B
	SVN REVISION: Not in version control	FILE NAME: PCB1.PcbDoc	APRV: EF	SCALE: 1:1

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ENGINEER:	TITLE:			
PCB DESIGNER: Eric Tarac				
DATE: 6/23/2022	PART NO:	REF: B	REV: B	
SVN REVISION: Not in version control	TYPE NAME: PCB1.PcbDoc	TYPE: EE	SCALE: 1:1	

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El Segundo, CA 90245

Layer	Name	Material	Thickness	Constant	Board Layer Stack
	Top Overlay				
	Top Solder	Solder Resist	0.40mil	3.5	
1	L1_Top		2.76mil		
	Core	FR-4	59.00mil	4.5	
2	L2_Bottom		2.76mil		
	Bottom Solder	Solder Resist	0.40mil	3.5	
	Bottom Overlay				

